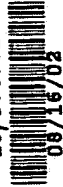


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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10064781	FILING DATE 08/16/2002	CLASS 330	SUBCLASS	GAU 2847	EXAMINER Brian Drula
**APPLICANTS: Lin Jyh-Fong; Hsieh Yi-Bin; Chien Chih-Chang; <div style="text-align: center; font-size: 1.5em;">369 124.110 2652</div>					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: TAIWAN 090125839 10/18/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials		BFD		VIAP0328USA	
TITLE : Signal processing circuit for optical disc drivers and the related method <small>U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
ISSUE FEE Amount Due Date Paid		Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner Application Examiner	
<input type="checkbox"/> PREPARED FOR ISSUE		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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